

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

<b>Applicant:</b>	Divakaruni et al.	<b>Conf. No.:</b>	1387
<b>Serial No.:</b>	10/707,388	<b>Art Unit:</b>	2891
<b>Filed:</b>	12/10/2003	<b>Examiner:</b>	Fulk, Steven J.
<b>Title:</b>	SILICIDE RESISTOR IN BEOL LAYER OF SEMICONDUCTOR DEVICE AND METHOD	<b>Docket No.:</b>	FIS920030274US1 (IBMF-0032)

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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**AFTER-FINAL REQUEST FOR RECONSIDERATION**

Sir:

**I. INTRODUCTORY COMMENTS:**

In response to the Final Office Action of April 11, 2008, please consider the following response: